	Туре	L #	Hits	Search Text	DBs	Time Stamp	Comment s
1	BRS	L2	45192 2	controller		2004/09/0 7 18:48	
2	BRS	L3	17920	control adj processor		2004/09/0 7 18:49	
3	BRS	L4	45921 5	2 or 3		2004/09/0 7 18:49	
4	BRS	L5	25710	system adj clock		2004/09/0 7 18:50	
5	BRS	L6	24278	multipl\$4 near5 clock	USPA T; US-P GPUB	2004/09/0 7 18:50	
6	BRS	L7	1924	5 same 6	USPĄ T; US-P GPUB	2004/09/0 7 18:53	
7	BRS	L8		(input adj3 output) or (I adj2 o)		2004/09/0 7 18:54	
8	BRS	L9	15269 0	synchronous\$2		2004/09/0 7 18:55	
9	BRS	L10	12300	8 same 9	USPA T; US-P GPUB	2004/09/0 7 18:55	
10	BRS	L11	146	4 and 7 and 10	USPA T; US-P GPUB	2004/09/0 7 18:56	
11	BRS	L12	1201	713/400.ccls. 713/401.ccls.	USPA T; US-P GPUB	2004/09/0 7 18:57	
12	BRS	L13	1216	713/500.ccls. 713/501.ccls.		2004/09/0 7 18:57	
13	BRS	L14	977	713/503.ccls. 713/600.ccls.	USPA T; US-P	2004/09/0 7 18:57	

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	Туре	L #	Hits	Search Text	DBs	Time Stamp	Comment s
14	BRS	L15	2750	10 10 14		2004/09/0 7 18:58	
15	BRS	L16	19	11 and 15		2004/09/0 7 18:58	

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